Attorney Docket No. 401322/SAKAI

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

KUROOKA et al.

Application No. Unknown

Art Unit: Unknown

Examiner: Unknown

Filed: August 3, 2001

For: TRACE CIRCUIT

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Prior to the examination of the above-identified patent application, please enter the following amendments and consider the following remarks.

IN THE SPECIFICATION:

Replace the paragraph beginning at page 1, line 4 with:

This invention relates to a trace circuit built into a debugging circuit which is, in turn, built into a microcomputer of a device.

Replace the paragraph beginning at page 1, line 20 with:

In a general microcomputer-embedded-typed LSI, the address bus and data bus and so on for connecting to the memory of the ICE are not connected to the LSI terminal. This is because the program is stored in a memory embedded in the microcomputer. Therefore, a mode only used for ICE connection is prepared. An address bus and a data bus are extended from the external terminal of the LSI. In addition, the original function, with the terminal used as an address bus and a data bus, is carried out within the ICE.

Replace the paragraph beginning at page 2, line 4 with:

However, because several terminals of the microcomputer need to be connected between the ICE and the target system, the connection between ICE and the target system has